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PATENT
0020-5176P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Hidenori MORIMOTO Conf.: 8674
Appl. No.: 10/656,286 / Group: 2815
Filed: September 8, 2003 Examiner: Eugene LEE
For: SEMICONDUCTOR CAPACITOR DEVICE HAVING
REDUCED VOLTAGE DEPENDENCE

LETTER REQUESTING WITHDRAWAL OF DRAWING OBJECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 1, 2004

Sir:

Applicant hereby requests withdrawal of the requirement for corrected drawings made in the Notice of Allowability of September 16, 2004.

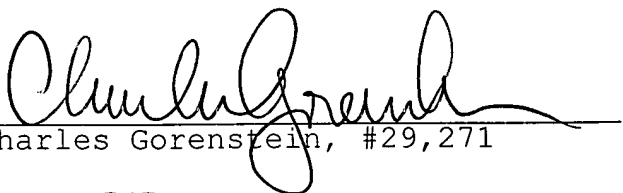
The Notice of Allowability objected to the drawings under 37 CFR 1.83(a) as allegedly not showing every feature of the claimed invention. The objection to the drawings states that, "the first MIM capacitors being electrically connected in inverse parallel with each other, and the second MIM capacitors being electrically connected in inverse parallel with each other must be shown." Applicant submits that this feature is shown in the drawings and thereby request that the objection be withdrawn.

Figure 3 and its associated circuit diagram shown in Figure 8 of the above-identified application, for example, show the claimed feature. In addition, the configuration shown in Figures 3 and 8 is described in paragraphs 0053 to 0057, in particular paragraph 0057. Based on the description in the specification, Figures 3 and 8 show two first MIM capacitors C1 electrically connected in inverse parallel and two second MIM capacitors C2 electrically connected in inverse parallel. Accordingly, the drawings do show the claimed feature.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

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